

Pulsonix V5.0 Release



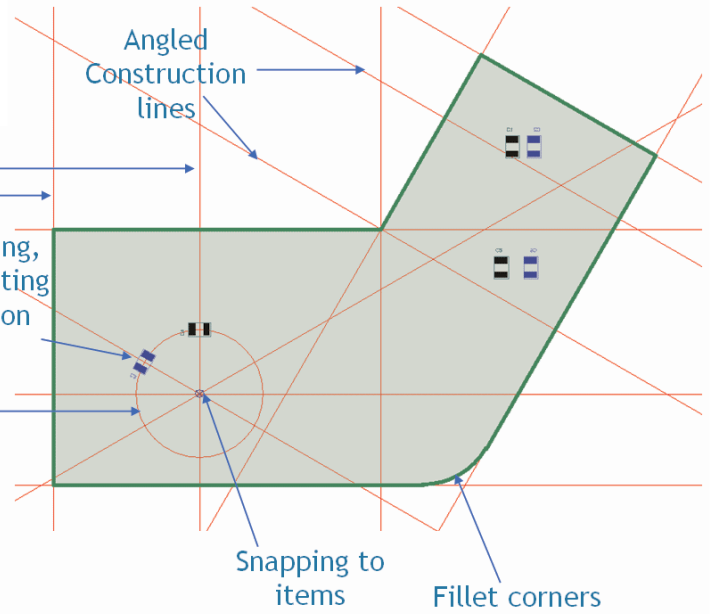
2D Construction Lines

With a level of sophistication like mechanical CAD systems, you can add construction lines to your design using the powerful tools provided. Once added, construction lines can be modified and manipulated. Design items can be snapped to construction lines and the 'regions' within construction lines used to auto-complete shape options. With ease-of-use and flexibility, this tool brings a new dimension of power to your PCB design tool. The construction line tool enables straight, angles, circle/arc lines plus a number of additional features to bisect, divide and tangent lines with existing construction lines or design items.

Construction lines

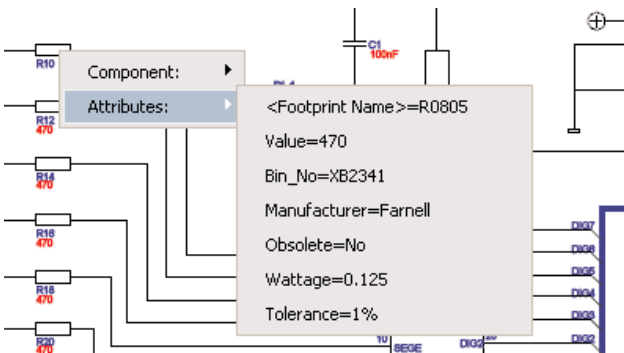
Comps snapping, moving & rotating to construction lines

Circular Construction lines



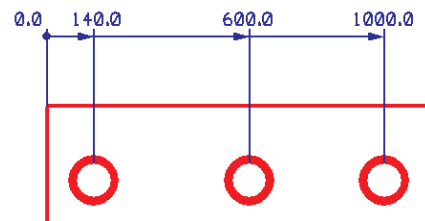
Direct Plotting to PDF

Without the need to install a separate Windows driver, Pulsonix is supplied with its own PDF driver to create active PDF files directly from Pulsonix. The PDF file can be created with choices of content ranging from a flat 'picture' to a fully active PDF file. This can contain active links showing net names, component names and attributes for example. As you hover your mouse over these items, their properties are revealed. From the Schematics, all your design sheets can be saved in PDF to one single 'design' file.



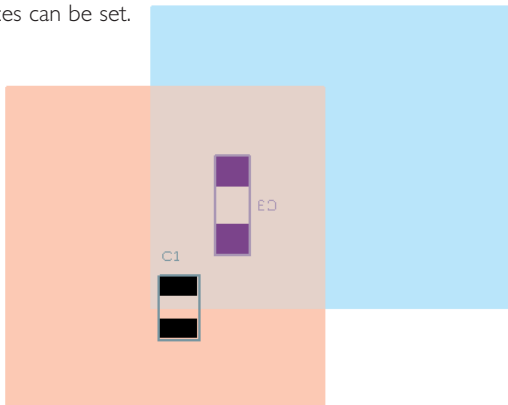
Attached Dimensions (or Dynamic Dimensions)

Attach dimensions to designs items and as they are moved your dimensions dynamically move along with them. Completely dynamic, all leader lines and dimension text are updated to reflect the new values. Dimensions can be unattached from one item and re-attached to another in one simple move.



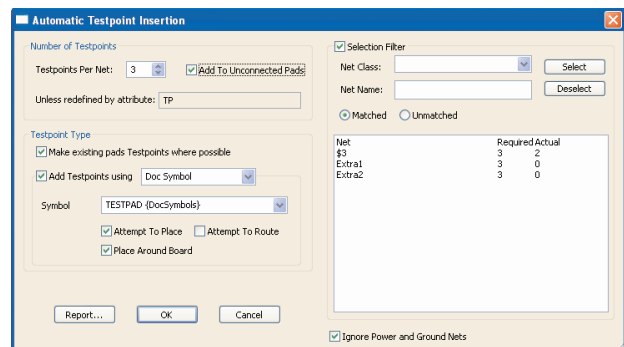
Transparent Copper

Copper shapes can be made transparent so that they can be layered in the design but with full view of design items underneath them. Transparent copper has total opacity control over the level visibility in the design, from solid to almost clear your own preferences can be set.



Net Testability Analyser

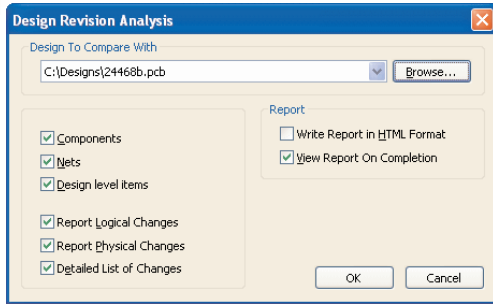
Analyse the nets in your design using constraint rules previously defined on the Technology Net Class or 'generic' rules defined on-the-fly in the dialog. Rules provided allow you to add testpoints using Parts, Pads, Vias and Doc Symbols and to define how the tool will attempt to place in the design. You can also allow the analyser to autoroute the connection between the net and the testpoint.



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Design Revision Analyser

Compare two designs (SCM>SCM, SCM>PCB, PCB>PCB) and the Design Revision Analyser will report all changes in detail. In PCB designs for example, this new tool will report Component positional changes, all track changes (position, style, thickness etc.) and layer/layer class differences, all in detail and all reported in item categories, Pads, Tracks, Components etc. The output reports are available in text and HTML.



Barcodes in Design

Add Barcodes to your PCB design using a special text font. All barcode parameters such as bar thickness and gaps can be easily defined.



Feature Summary Pulsonix V5.0

- Barcodes
- Replicate item
- Rearrange Multiple Selected items
- Trim/Extend item
- Import CSV files into Parts Editor
- DxDDesigner Import
- System Designer Import
- Network licensing improvements
- Next Symbol Previews
- View Footprint in Insert SCM Component/Connectors
- Alternate Symbols for Connector Parts
- Deleted Unused in Tech files
- Shrink & Grow Solder/Paste Mask layers by %
- User Pad Shape Improvements
- Project Environment Variables
- Track Backoff for 'T' junctions & Necked tracks
- Pulsonix Spice update
- New Pulsonix PLM Integration product
- Plus over 50 new additional features and enhancements

Slotted pads

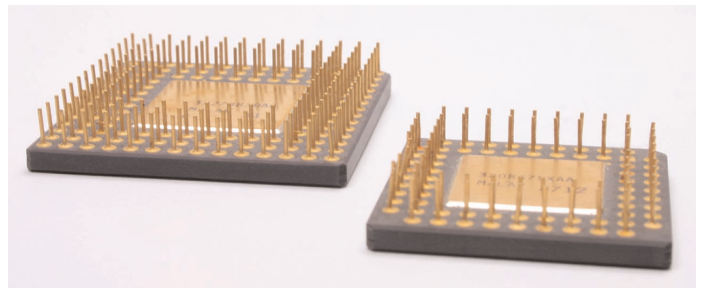
Pulsonix now adds the ability to create slotted pads with Technology files and user defined pads. Slots are not restricted in shape and can be any shape you can create. You can also pick from the standard list of shapes available as well. The outputs to manufacturing will recognise the slotted pads and understand how to handle them correctly. Outputs for different types of slots are available ranging from drilling to milling to X,Y location information for punch machines.



FPGA Support

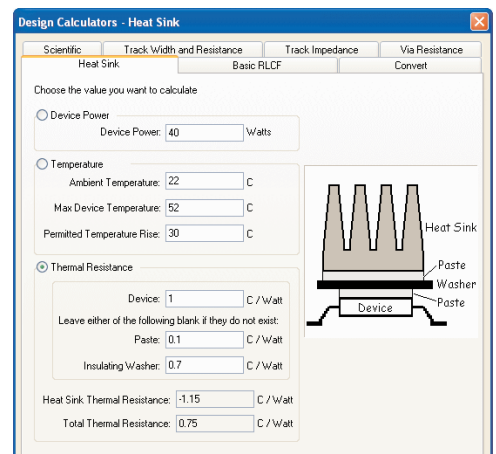
Pulsonix FPGA is a new interface within Pulsonix to allow export and import of FPGA pin data from the FPGA vendors' tools. Pulsonix FPGA supports the Altera Quartus II and Xilinx ISE development tools initially as well as generic 'CSV format files. Support for other FPGA development tools will follow.

Part data is imported from the FPGA environment, or you can export Part pin data from Pulsonix to the FPGA tool; the starting point is flexible. You can create the pin mapping for Pulsonix using an import pin list if you like. Once the pin data is in the FPGA design tool, your FPGA is created and the newly configured pin data back annotated into the Part in Pulsonix. The original Part definition is retained for subsequent use. Each variant of the same FPGA Part in design is unique based on its internal configuration thus allowing multiple FPGAs of the same type on the same design.



Design Calculators

This new option contains seven calculators for use in your design process to calculate: Heat Sinks, RLCF, Scientific, Track Impedance, Track Width, Via Resistance and a Metric / Imperial calculator.



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